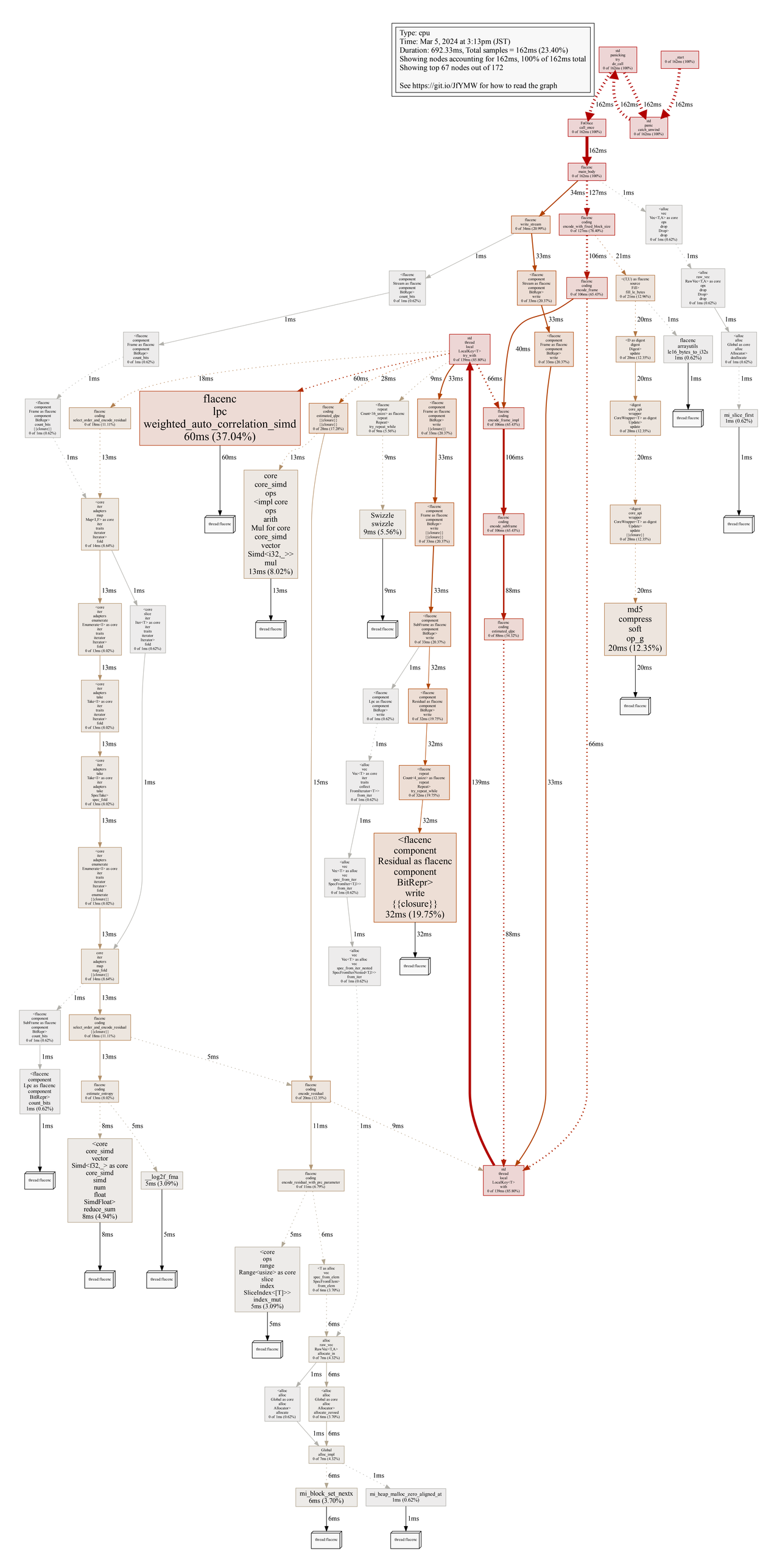
Type: cpu
Time: Mar 5, 2024 at 3:13pm (JST)
Duration: 692.33ms, Total samples = 162ms (23.40%)
Showing nodes accounting for 162ms, 100% of 162ms total
Showing top 67 nodes out of 172
See https://git.io/JfYMW for how to read the graph
std panicking try do_call 0 of 162ms (100%)
start 0 of 162ms (100%)
162ms
std panic catch_unwind 0 of 162ms (100%)
FnOnce call_once 0 of 162ms (100%)
flacenc main_body 0 of 162ms (100%)
34ms
127ms
1ms
flacenc write_stream 0 of 34ms (20.99%)
flacenc coding encode_with_fixed_block_size 0 of 127ms (78.40%)
<alloc vec Vec<T,A> as core ops drop Drop> drop 0 of 1ms (0.62%)
33ms
106ms
21ms
<flacenc component Stream as flacenc component BitRepr> write 0 of 33ms (20.37%)
flacenc coding encode_frame 0 of 106ms (65.43%)
<T,U> as flacenc source Fill> fill_le_bytes 0 of 21ms (12.96%)
<alloc raw_vec RawVec<T,A> as core ops drop Drop> drop 0 of 1ms (0.62%)
<flacenc component Stream as flacenc component BitRepr> count_bits 0 of 1ms (0.62%)
40ms
<flacenc component Frame as flacenc component BitRepr> write 0 of 33ms (20.37%)
20ms
<D as digest Digest> Digest> update 0 of 20ms (12.35%)
flacenc arrayutils le16_bytes_to_i32s 1ms (0.62%)
<alloc alloc Global as core alloc Allocator> deallocate 0 of 1ms (0.62%)
<flacenc component Frame as flacenc component BitRepr> count_bits 0 of 1ms (0.62%)
std thread local LocalKey<T> try_with 0 of 139ms (85.80%)
18ms
60ms
28ms
9ms
66ms
flacenc coding encode_frame_impl 0 of 106ms (65.43%)
<digest core_api wrapper CoreWrapper<T> as digest Update> update 0 of 20ms (12.35%)
mi_slice_first 1ms (0.62%)
<flacenc component Frame as flacenc component BitRepr> count_bits 0 of 1ms (0.62%)
flacenc coding select_order_and_encode_residual 0 of 18ms (11.11%)
flacenc
lpc
weighted_auto_correlation_simd
60ms (37.04%)
flacenc coding estimated_qlpc {{closure}} {{closure}} 0 of 28ms (17.28%)
<flacenc component Count<16_usize> as flacenc component Repeat> try_repeat_while 0 of 9ms (5.56%)
<flacenc component Frame as flacenc component BitRepr> write {{closure}} {{closure}} 0 of 33ms (20.37%)
13ms
core
core_simd
ops
<impl core
ops
arith
Mul for core
core_simd
vector
Simd<i32,_>>
mul
13ms (8.02%)
Swizzle
swizzle
9ms (5.56%)
<flacenc component Frame as flacenc component BitRepr> write {{closure}} {{closure}} 0 of 33ms (20.37%)
flacenc coding encode_subframe 0 of 106ms (65.43%)
<digest core_api wrapper CoreWrapper<T> as digest Update> update {{closure}} 0 of 20ms (12.35%)
thread.flacenc
<core iter adapters map Map<T,F> as core iter traits iterator Iterator> fold 0 of 14ms (8.64%)
<core iter adapters enumerate Enumerate<I> as core iter traits iterator Iterator> fold 0 of 13ms (8.02%)
<core slice iter Iter<T> as core iter traits iterator Iterator> fold 0 of 1ms (0.62%)
<core iter adapters take Take<I> as core iter traits iterator Iterator> fold 0 of 13ms (8.02%)
<core iter adapters take Take<I> as core iter adapters take SpecTake> spec_fold 0 of 13ms (8.02%)
<core iter adapters enumerate Enumerate<I> as core iter traits iterator Iterator> fold enumerate {{closure}} 0 of 13ms (8.02%)
<flacenc component SubFrame as flacenc component BitRepr> write 0 of 33ms (20.37%)
flacenc coding estimated_qlpc 0 of 88ms (54.32%)
<flacenc component Lpc as flacenc component BitRepr> write 0 of 1ms (0.62%)
<flacenc component Residual as flacenc component BitRepr> write 0 of 32ms (19.75%)
<alloc vec Vec<T> as core iter traits collect FromIterator<T>> from_iter 0 of 1ms (0.62%)
<flacenc repeat Count<4_usize> as flacenc repeat Repeat> try_repeat_while 0 of 32ms (19.75%)
15ms
139ms
33ms
66ms
<alloc vec Vec<T> as alloc vec spec_from_iter SpecFromIter<T,I>> from_iter 0 of 1ms (0.62%)
<flacenc component Residual as flacenc component BitRepr> write {{closure}} 32ms (19.75%)
88ms
<alloc vec Vec<T> as alloc vec spec_from_iter_nested SpecFromIterNested<T,I>> from_iter 0 of 1ms (0.62%)
<flacenc component SubFrame as flacenc component BitRepr> count_bits 0 of 1ms (0.62%)
<flacenc component Lpc as flacenc component BitRepr> count_bits 1ms (0.62%)
flacenc coding estimate_entropy 0 of 13ms (8.02%)
flacenc coding encode_residual 0 of 20ms (12.35%)
<core core_simd vector Simd<f32,_> as core core_simd simd num float SimdFloat> reduce_sum 8ms (4.94%)
__log2f_fma 5ms (3.09%)
flacenc coding encode_residual_with_prc_parameter 0 of 11ms (6.79%)
<core ops range Range<usize> as core slice index SliceIndex<[T]>> index_mut 5ms (3.09%)
<T as alloc vec spec_from_elem SpecFromElem> from_elem 0 of 6ms (3.70%)
alloc raw_vec RawVec<T,A> allocate_in 0 of 7ms (4.32%)
<alloc alloc Global as core alloc Allocator> allocate 0 of 1ms (0.62%)
<alloc alloc Global as core alloc Allocator> allocate_zeroed 0 of 6ms (3.70%)
Global alloc_impl 0 of 7ms (4.32%)
mi_block_set_nextx 6ms (3.70%)
mi_heap_malloc_zero_aligned_at 1ms (0.62%)